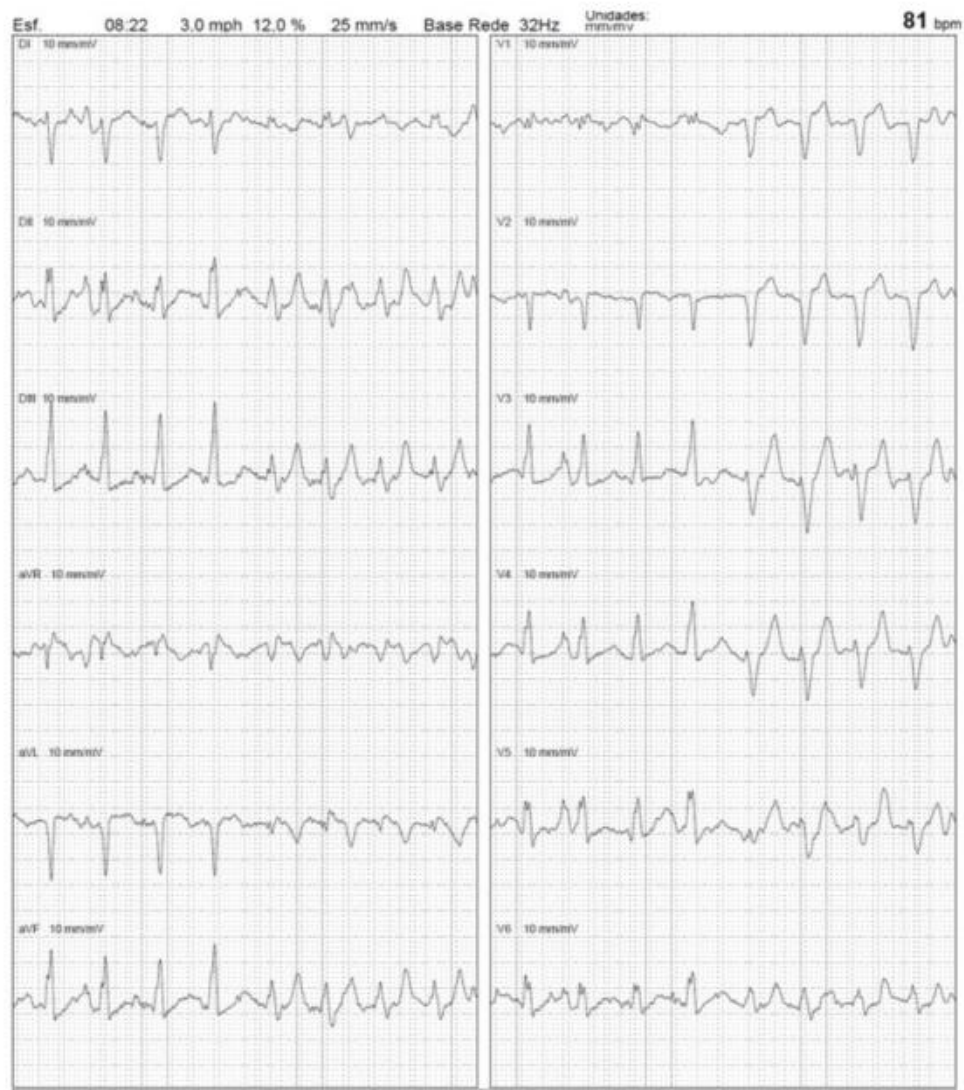




D1
D2
D3
AVF
V1
V2
V6
SCp
SC4
SC3
SC2
SCd
RFD

